



DDR SDRAM REGISTERED DIMM MODULE, 2.5V 512MByte - 64MX72 AVM7264R52C5266K1-AP

FEATURES

JEDEC DDR 266MHz PC2100, Lead Free, RoHS compliant

- Clock frequency: 133MHz with CAS latency 2.5
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 30.48mm (1.20 inch) height

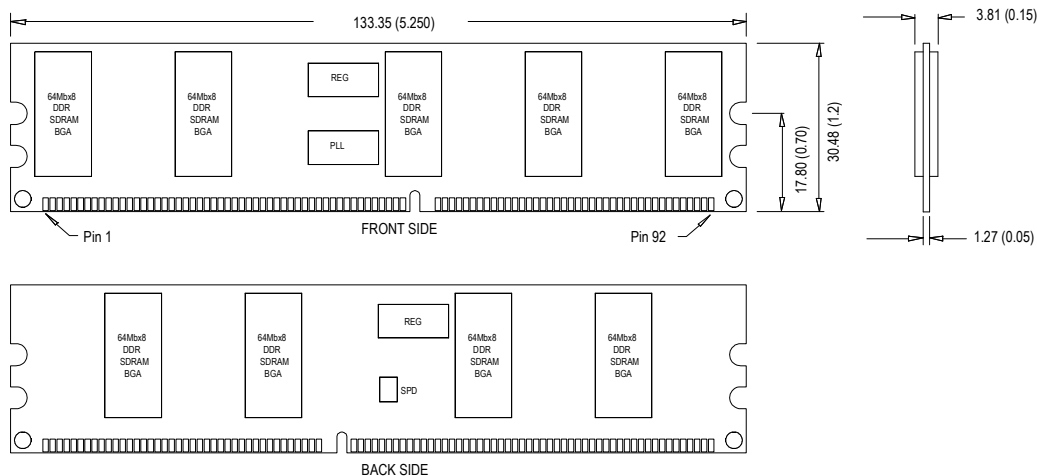
DESCRIPTION

The AVM7264R52C5266K1-AP is a Registered DDR SDRAM DIMM module. This module is JEDEC Pinout compatible DDR SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM7264R52C5266K1-AP memory module is 512MByte and organized as 64MX72 ECC array using (9) 64MX8 DDR SDRAMs in lead-free TSSOP II packages

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	M=184-PIN DDR DIMM	72=X72	64 = 64M	R=REGISTERED	52=16Mx8x4 (DDR SDRAM)	C=2.5V	5 = CAS LATENCY 2.5	266MHz	K=DDR Sdram	REV=1

Other options may be available. Call for specific part number information on options not listed.



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