

FEATURES

JEDEC Compliant DDR 333MHz Version 1.0

- Clock frequency: 166MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 1.20 inch height

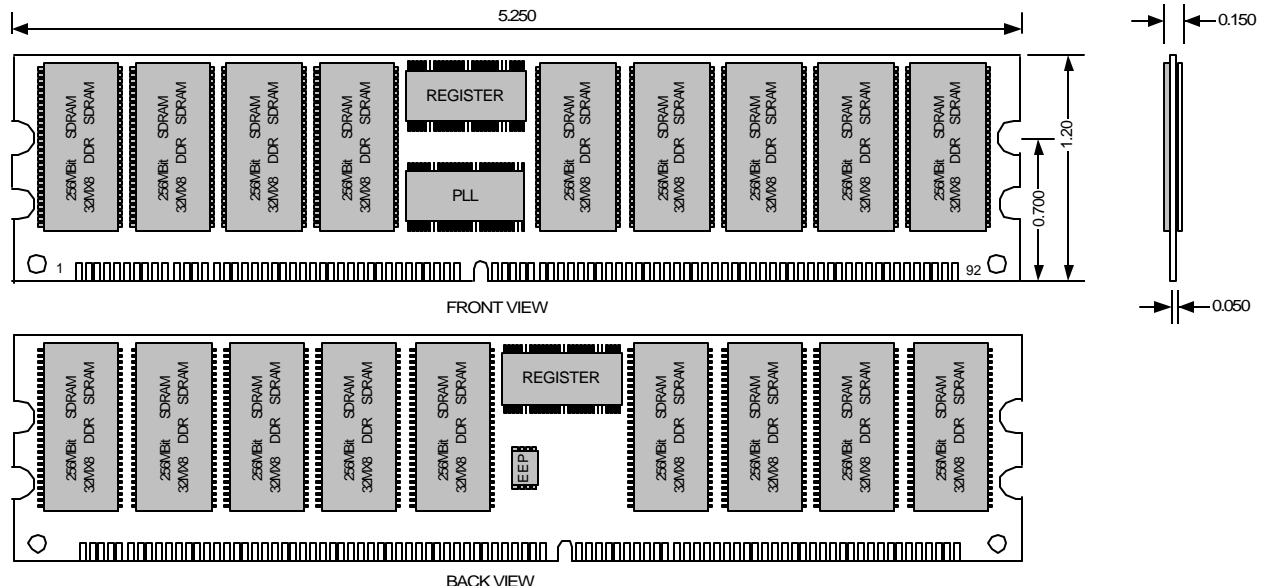
DESCRIPTION

The AVM7264R39C5333K1 is a Registered DDR SDRAM DIMM module. This module is JEDEC Pinout compatible DDR SDRAM Registered DIMM. The module has all the addresses and control signals buffered to reduce capacitive loading. The module utilizes a phase lock loop to reduce the capacitive loading on the clock signals and to synchronize all SDRAM input clocks with the system clock. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM7264R39C5333K1 memory module is 512MByte and organized as 64MX72 ECC array using (18) 32MX8 DDR SDRAMs in TSSOP II packages.

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



Avant Ordering Guides

| AV | M | 72 | 64 | R | 39 | C | 5 | 333 | K | 1 |
|-----------|--------------------|--------|---------|--------------|--------------------------|--------|-------------------|--------|-------------|-------|
| INVENTORY | MOD. TYPE | ORG. | DENSITY | PARITY | TYPE | VOLT. | FEATURE | SPEED | MODE | REV |
| AV=AVANT | M=184-PIN DDR DIMM | 72=X72 | 64=64M | R=REGISTERED | 39=8192x4 (DDR SDRAM) | C=2.5V | 5=CAS LATENCY 2.5 | 333MHz | K=DDR SDRAM | REV=1 |

Other options may be available. Call for specific part number information on options not listed.



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