



DDR SDRAM DIMM MODULE, 2.5V 512MByte - 64MX64 AVM6464U51C5266K2-AP

FEATURES

JEDEC DDR-266 PC2100, Lead Free, RoHS compliant

- Clock frequency: 133MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Low active and standby current consumption
- 8K refresh per 64ms
- Auto refresh and self refresh capability
- Gold card edge fingers
- Double-sided module
- 29.46mm (1.16 inch) height

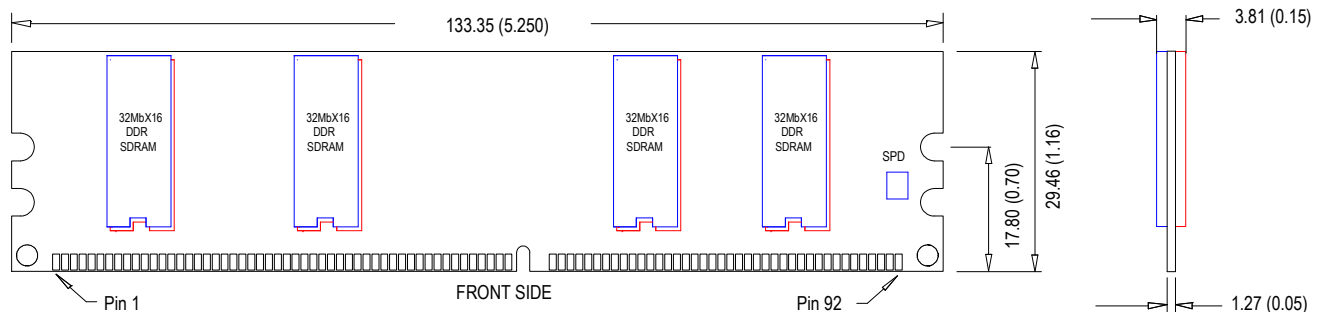
DESCRIPTION

The AVM6464U51C5266K2-AP is a 184 pin Unbuffered DDR SDRAM DIMM module. This module is JEDEC pinout compatible. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM6464U51C5266K2-AP memory module is 512MByte and organized as 64MX64 array using (8) 32MX16 DDR SDRAMs in lead-free, TSSOP II packages.

This memory module is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



- NOTES: 1- All dimensions are in millimeters (inches)
2- All blue ICs are on the front, and all red ICs are on the back side of the module.
3- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLTAGE	FEATURE	SPEED	MODE	REV
AV = AVANT	M = 184-PIN DDR DIMM	64 = X64	64 = 64M	U=UNBUFFERED	51 = 8Mx16x4 (DDR SDRAM)	C = 2.5V	5 = CAS LATENCY 2.5	266MHz	K = DDR SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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