



DDR SDRAM DIMM MODULE, 2.5V 1GByte - 128Mx64 AVM6428U61C3400K8-AP

FEATURES

JEDEC DDR 400MHz PC3200 Version 1.0, Lead Free, RoHS compliant

- Clock frequency: 200MHz with CAS latency 3
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 3
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Single-sided module
- 29.46mm (1.16 inch) height

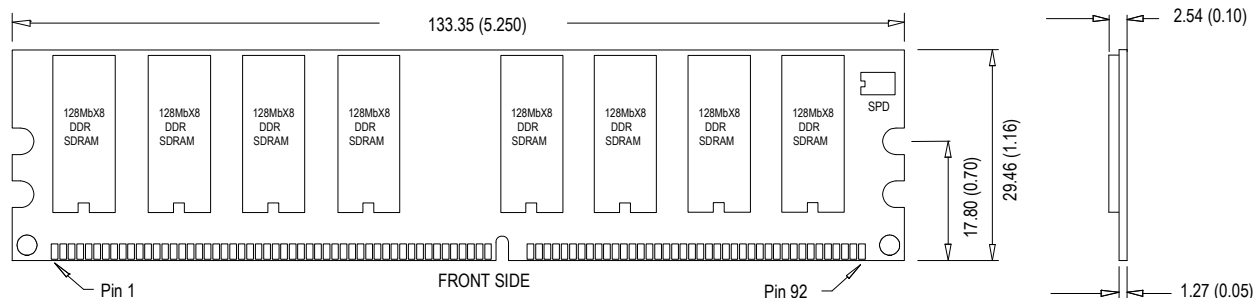
DESCRIPTION

The AVM6428U61C3400K8-AP is a 184 pin Unbuffered DDR SDRAM DIMM memory module. This module is JEDEC Pinout compatible. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM6428U61C3400K8-AP memory module is 1GMBYTE and organized as 128Mx64 array using (8) 128Mx8 DDR SDRAMs in lead free TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	M	64	28	U	61	C	3	400	K	8
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLTAGE	FEATURE	SPEED	MODE	REV
AV = AVANT	M =184-PIN DDR DIMM	64 = X64	28 = 128M	U=UNBUFFERED	61 = 16Mx8x8 (DDR SDRAM)	C = 2.5V	3 = CAS LATENCY 3	400MHz	K = DDR	REV=8

Other options may be available. Call for specific part number information on options not listed.



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