



DDR SDRAM DIMM MODULE, 2.5V 64MByte - 8MX64 AVM6408U35C5266K2-AP

FEATURES

JEDEC DDR 266MHz PC2100 Version 1.0, Lead Free, RoHS compliant

- Clock frequency: 166MHz with CAS latency 2.5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 2, 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 2.5
- Auto refresh and self refresh capability
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- SSTL-2 compatible inputs and outputs
- Decoupling capacitors at each memory device
- Double-sided module
- 29.46mm (1.16 inch) height

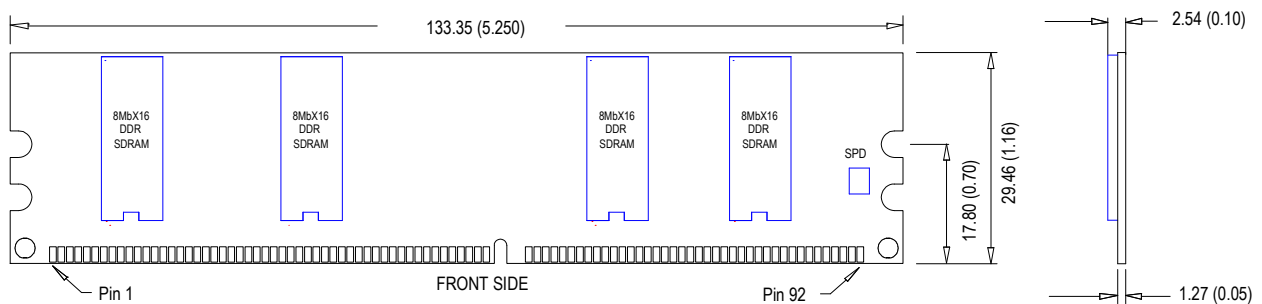
DESCRIPTION

The AVM6408U35C5266K2-AP is a 184 pin Unbuffered DDR SDRAM DIMM module. This module is JEDEC pinout compatible. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVM6408U35C5266K2-AP memory module is 64MByte and organized as 8MX64 array using (4) 8MX16 DDR SDRAMs in lead free TSSOP II packages.

This memory module is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLTAGE	FEATURE	SPEED	MODE	REV
AV = AVANT	M = 184-PIN DDR DIMM	64 = X64	08 = 08M	U=UNBUFFERED	35 = 2Mx16x4 (DDR SDRAM)	C = 2.5V	5 = CAS LATENCY 2.5	266MHZ	K = DDR	REV=2

Other options may be available. Call for specific part number information on options not listed.



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