



# DDR3 SDRAM UNBUFFERED SODIMM MODULE, 1.5V 2GByte - 256MX72 ECC AVH7256U64F9333GC-MTDP

## FEATURES

JEDEC DDR3 PC3-10600 1333MT/s, Lead Free, RoHS compliant

- Clock frequency: 667MHz with CAS latency 9
- 256 byte serial EEPROM
- Data input and output masking
- Programmable Partial Array Self-Refresh (PASR)
- Programmable Output driver impedance control
- Programmable CAS latency: 9
- Burst lengths (BL): 8 and 4 with Burst Chop (BC)
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination (ODT)
- Auto refresh and self refresh capability
- Double-sided module
- 30mm (1.181 inch) height

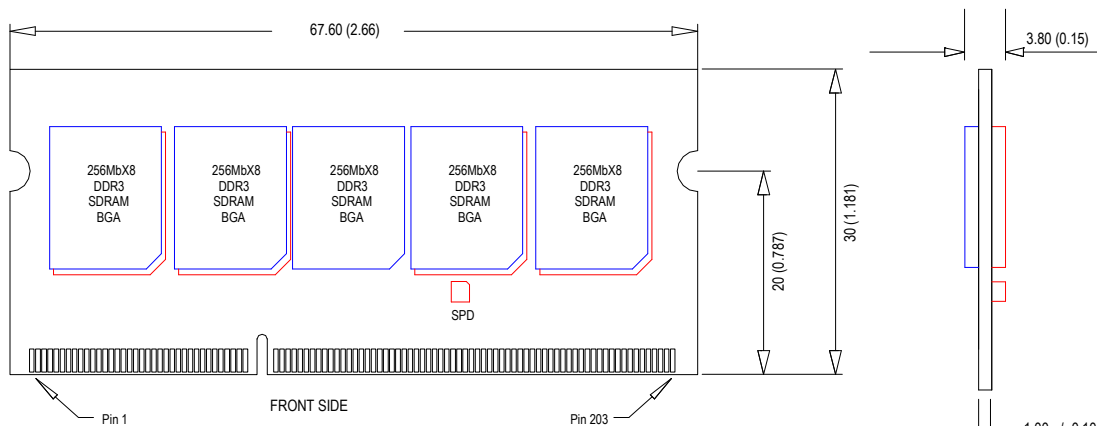
## DESCRIPTION

The AVH7256U64F9333GC-MTDP is an Unbuffered DDR3 SDRAM SODIMM module. This module is JEDEC MO-268 R/C C DDR3 SDRAM Unbuffered SODIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVH7256U64F9333GC-MTDP memory module is 2GByte and organized as 256MX72 ECC array using (9) 256MX8 DDR3 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

## PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)  
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.  
3- All blue ICs are on the front, and all red ICs are on the back side of the module.

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INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	H = 204-PIN SO-DIMM	72 = X72	56 = 256M	U=UNBUFFERED	64 = 32Mx8x8 (DDR3 SDRAM)	F = 1.5V	9 = CAS LATENCY 9	1333MT/s	G = DDR3 SDRAM	REV=C

Other options may be available. Call for specific part number information on options not listed.



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