



# DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V 512MByte - 64MX72 ECC AVF7264U39E4533F2-AP

## FEATURES

JEDEC DDR2 PC2-4200 533MT/s, Lead Free, RoHS compliant

- Clock frequency: 266MHz with CAS latency 4
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 4
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 30mm (1.18 inch) height

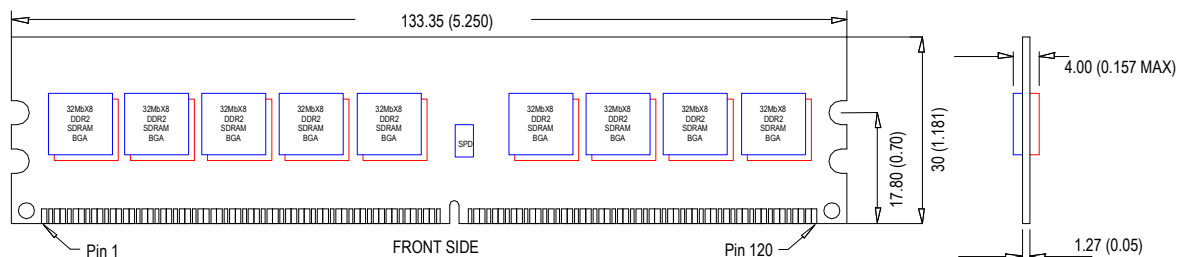
## DESCRIPTION

The AVF7264U39E4533F2-AP is an Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC MO-237 R/C B DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF7264U39E4533F2-AP memory module is 512MByte and organized as 64MX72 array using (18) 32MX8 DDR2 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

## PHYSICAL DIMENSIONS



- NOTES: 1- All dimensions are in millimeters (inches)  
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.  
3- All blue ICs are on the front, and all red ICs are on the back side of the module.

**Avant Ordering Guides**

<b>AV</b>	<b>F</b>	<b>72</b>	<b>64</b>	<b>U</b>	<b>39</b>	<b>E</b>	<b>4</b>	<b>533</b>	<b>F</b>	<b>2</b>
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	F = 240-PIN DDR2 DIMM	72 = X72	64 = 64M	U=UNBUFFERED	39 = 8Mx8x4 (DDR2 SDRAM)	E=1.8V	4 = CAS LATENCY 4	533MT/s	F = DDR2 SDRAM	REV=2

Other options may be available. Call for specific part number information on options not listed.



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