



DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V 512MByte - 64MX64 AVF6464U52E5800FB-AP

FEATURES

JEDEC DDR2 PC2-6400 800MT/s, Lead Free, RoHS compliant

- Clock frequency: 400MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Single-sided module
- 18.30mm (0.72 inch) height

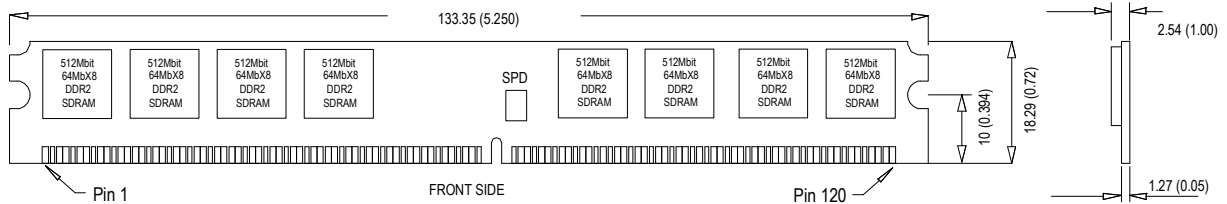
DESCRIPTION

The AVF6464U52E5800FB-AP is a Very Low Profile (VLP) Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC Pinout compatible DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF6464U52E5800FB-AP memory module is 512MByte and organized as 64MX64 array using (8) 64MX8 DDR2 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, eight-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	F	64	64	U	52	E	5	800	F	B
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	F = 240-PIN DDR2 DIMM	64 = X64	64 = 64M	U=UNBUFFERED	52 = 16Mx8x4 (DDR2 SDRAM)	E = 1.8V	5 = CAS LATENCY 5	800MT/s	F = DDR2 SDRAM	REV=B

Other options may be available. Call for specific part number information on options not listed.



Avant™ Technology LP., reserves the right to change products or specifications without notice.