



DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V

512MByte - 64MX64

AVF6464U51E5667F3-AP

FEATURES

JEDEC DDR2 PC2-5300 667MT/s, Lead Free, RoHS compliant

- Clock frequency: 333MHz with CAS latency 5
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 5
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 30mm (1.18 inch) height

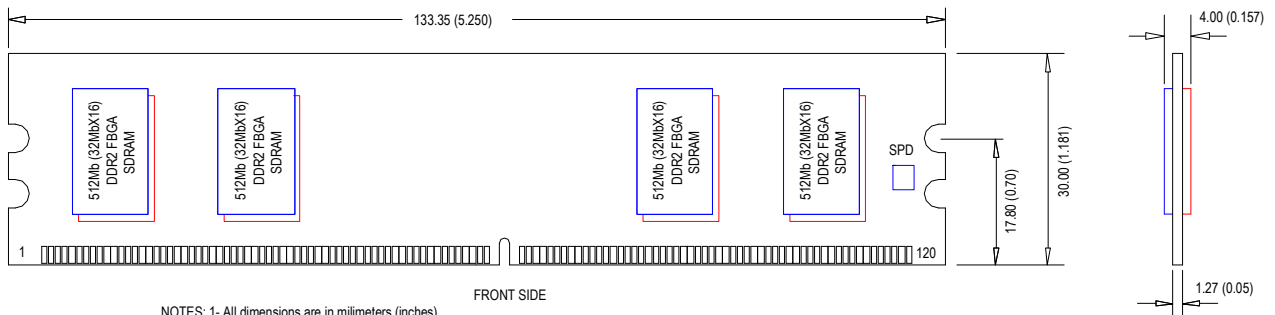
DESCRIPTION

The AVF6464U51E5667F3-AP is an Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC MO-237 R/C H DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF6464U51E5667F3-AP memory module is 512MByte and organized as 64MX64 array using (8) 32MX16 DDR2 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)
3- All blue ICs are on the front, and all red ICs are on the back side of the module.
3- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

Avant Ordering Guides

AV	F	64	64	U	51	E	5	667	F	3
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	F = 240-PIN DDR2 DIMM	64 = X64	64 = 64M	U=UNBUFFERED	51 = 8Mx16x4 (DDR2 SDRAM)	E = 1.8V	5 = CAS LATENCY 5	667MT/s	F = DDR2 SDRAM	REV=3

Other options may be available. Call for specific part number information on options not listed.



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