



# DDR2 SDRAM UNBUFFERED DIMM MODULE, 1.8V

## 1GByte - 128MX64

### AVF6428U60E6800F3-AP

## FEATURES

JEDEC DDR2 PC2-6400 800MT/s, Lead Free, RoHS compliant

- Clock frequency: 400MHz with CAS latency 6
- 256 byte serial EEPROM
- Data input and output masking
- Programmable burst length: 4, 8
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 6
- Bi-directional Differential Data-Strobe
- Gold card edge fingers
- 8K refresh per 64ms
- Low active and standby current consumption
- On Die Termination
- Auto refresh and self refresh capability
- Double-sided module
- 30mm (1.18 inch) height

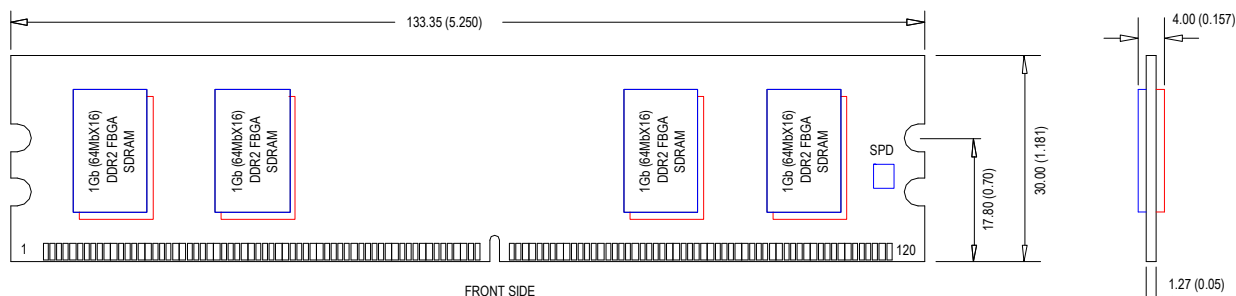
## DESCRIPTION

The AVF6428U60E6800F3-AP is an Unbuffered DDR2 SDRAM DIMM module. This module is JEDEC MO-237 R/C H DDR2 SDRAM Unbuffered DIMM. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVF6428U60E6800F3-AP memory module is 1GByte and organized as 128MX64 array using (8) 64MX16 DDR2 SDRAMs in lead-free FBGA packages.

The module PCB is fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

## PHYSICAL DIMENSIONS



NOTES: 1- All dimensions are in millimeters (inches)  
3- All blue ICs are on the front, and all red ICs are on the back side of the module.  
3- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.

**Avant Ordering Guides**

<b>AV</b>	<b>F</b>	<b>64</b>	<b>28</b>	<b>U</b>	<b>60</b>	<b>E</b>	<b>6</b>	<b>800</b>	<b>F</b>	<b>3</b>
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV = AVANT	F = 240-PIN DDR2 DIMM	64 = X64	28 = 128M	U=UNBUFFERED	60 = 8Mx16x8 (DDR2 SDRAM)	E = 1.8V	6 = CAS LATENCY 6	800MT/s	F = DDR2 SDRAM	REV=3

Other options may be available. Call for specific part number information on options not listed.



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