



# SDRAM DIMM MODULE, 3.3V 512MByte - 64MX64 AVE6464U39A3133E3

## FEATURES

INTEL PC133 Version 1.0

- Clock frequency: 133MHz with CAS latency 3
- Burst read/write and burst read/single write capability
- Data input and output masking
- Programmable burst length: 1, 2, 4, 8 and full page
- Programmable burst type: sequential and interleave
- Programmable CAS latency: 3
- Auto refresh and self refresh capability
- 8K refresh per 64ms
- 256 byte serial EEPROM
- LVTTTL-compatible inputs and outputs
- Low active and standby current consumption
- Decoupling capacitors at each memory device
- Double-sided module
- Gold card edge fingers
- 29.21mm (1.15 inch) height

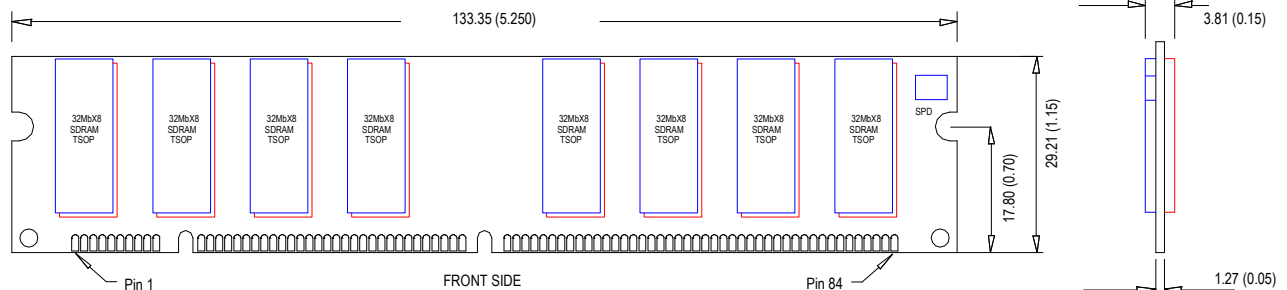
## DESCRIPTION

The AVE6464U39A3133E3 is an Unbuffered SDRAM DIMM memory module. This module is based on INTEL PC133 SDRAM DIMM version 1.0. A 256 byte serial EEPROM on board can be used to store module information such as timing, configuration, density, etc.

The AVE6464U39A3133E3 memory module is 512MByte and organized as 64MX64 array using (16) 32MX8 SDRAMs in TSSOP II packages.

All memory modules are fabricated using the latest technology design, six-layer printed circuit board substrate construction with low ESR decoupling capacitors on-board for high reliability and low noise.

## PHYSICAL DIMENSIONS



- NOTES: 1- All dimensions are in millimeters (inches)  
2- The dimensional drawings are for reference only. Refer to the JEDEC document for additional information.  
3- All blue ICs are on the front, and all red ICs are on the back side of the module.

**Avant Ordering Guides**

<b>AV</b>	<b>E</b>	<b>64</b>	<b>64</b>	<b>U</b>	<b>39</b>	<b>A</b>	<b>3</b>	<b>133</b>	<b>E</b>	<b>3</b>
INVENTORY	MOD. TYPE	ORG.	DENSITY	PARITY	TYPE	VOLT.	FEATURE	SPEED	MODE	REV
AV=AVANT	E=168 PIN DIMM	64=X64	64=64M	U=UNBUFFERED	39=8Mx8x4 (8K)	A=3.3V	3 = CAS LATENCY 3	133MHz	E=SYNC	REV=3

Other options may be available. Call for specific part number information on options not listed.



Avant™ Technology LP., reserves the right to change products or specifications without notice.